Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	170	(protect protection) and (((257/393) or (257/903-904)).CCLS.)	USPAT	OR	ON	2005/06/21 10:59
L2	27599	sram (static adj random adj access adj memory)	USPAT	OR	ON	2005/06/21 12:37
L3	905	(adding added) with nmos	USPAT	OR	ON	2005/06/21 12:37
L4	20	L3 same L2	USPAT	OR	ON	2005/06/21 12:37
L5	62	(sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/21 13:15
S1	1	("6756692").PN.	USPAT; USOCR	OR	OFF	2004/11/18 10:57
S2	1	S1 and (resistance adj adding adj transistor)	USPAT	OR	ON	2004/09/27 08:19
S3	25159	sram (static adj random adj access adj memory)	USPAT	OR	ON	2004/09/27 08:59
S4	1	S3 and (resistance adj adding adj transistor)	USPAT	OR	ON	2004/09/27 09:05
S5	774	((257/393) or (257/903-904)).CCLS.	USPAT; USOCR	OR	OFF	2004/09/27 08:36
S6	1	S5 and (resistance adj adding adj transistor)	USPAT	OR	ON	2004/09/27 08:37
S7	930	nmos same S3	USPAT	OR	ON	2004/09/27 08:38
S8	821	(adding added) with nmos	USPAT	OR	ON	2004/09/27 09:07
S9	18	S8 same S3	USPAT	OR	ON	2005/06/21 12:37
S10	116	S8 and S3	USPAT	OR	ON	2004/09/27 08:39
S11	24284	sram (static adj random adj access adj memory)	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/27 09:05
S12	2	S11 and (resistance adj adding adj transistor)	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/27 09:05
S13	440	(adding added) with nmos	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/27 09:07
S14	61	S11 and S13	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/27 09:14
S15	4	S5 and S8	USPAT	OR	ON	2004/09/27 09:14

S24	21447	sram (static adj random adj access adj memory)	USPAT	OR	ON	2004/09/27 08:18
S25	29318	(metal adj semiconductor) silicide	USPAT	OR	ON	2003/08/27 13:42
S26	361	(sram (static adj random adj access adj memory)) same ((metal adj semiconductor) silicide)	USPAT	OR	ON	2003/08/27 10:23
S27	505403	protect protection	USPAT	OR	ON	2003/08/27 13:43
S28	9	((sram (static adj random adj access adj memory)) same ((metal adj semiconductor) silicide)) same (protect protection)	USPAT	OR	ON	2003/08/27 10:25
S29	137	(sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection))	USPAT	OR	ON	2004/01/21 14:04
S30	712	((257/393) or (257/903-904)). CCLS.	USPAT; USOCR	OR	OFF	2004/09/27 08:36
S31	158	(protect protection) and ((((257/393) or (257/903-904)). CCLS.)	USPAT	OR	ON	2005/06/21 10:58
S32	144	((protect protection) and ((((257/393) or (257/903-904)). CCLS.)) not ((sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection)))	USPAT	OR	ON	2004/01/21 14:09
S33	296	((metal adj semiconductor) silicide) and ((((257/393) or (257/903-904)).CCLS.)	USPAT	OR	ON	2004/01/21 14:11
S34	249	(sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) and ((((257/393) or (257/903-904)). CCLS.))	USPAT	OR	ON	2003/08/27 13:35
S35	18021	sram (static adj random adj access adj memory)	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/27 13:42
S36	32841	(metal adj semiconductor) silicide	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/27 13:43
S37	533133	protect protection	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/27 13:43

S38	36	(sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/21 13:15
S39	177	(sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) and (protect protection)) not ((sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection)))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/21 15:05
S40	142	(sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection))	USPAT	OR	ON	2004/01/21 14:05
S41	148	((protect protection) and (((257/393) or (257/903-904)).CCLS.)) not ((sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection)))	USPAT	OR	ON	2004/01/21 14:09
S42	309	((metal adj semiconductor) silicide) and (((257/393) or (257/903-904)).CCLS.)	USPAT	OR	ON	2004/01/21 14:11
S43	37	(sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/21 15:05
S44	203	(sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) and (protect protection)) not ((sram (static adj random adj access adj memory)) and (((metal adj semiconductor) silicide) same (protect protection)))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/01/21 15:05
S45	1	US-3395783-$.DID.	USPAT	OR	ON	2004/03/11 13:12
S46	1	US-6395783-$.DID.	USPAT	OR	ON	2004/03/11 13:13
S47	1	US-5395783-$.DID.	USPAT	OR	ON	2004/03/11 13:13
S48	2	US-5338963-$.DID. OR US-4956815-$.DID.	USPAT	OR	ON	2004/11/18 11:03
S49	1	"6756692".pn. and load	USPAT	OR	ON	2004/11/18 11:04